



Mrs. Vandana mahanthi

Associate Professor

Department

ECE

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Academic Background

Pursuing Ph.D. (VLSI)-GITAM Deemed to Be university

M.Tech (VLSI) – Avanthi Institute of Engineering & Technology, JNTUK

M.Tech (CSE)-ANDHRA University

B. Tech (Electronics & Communication Engineering) – St. Theresa Institute of Engineering & Technology- JNTUH

Mrs. Vandana mahanthi has 13 years of experience in the field of Electronics & Communication Engineering. She has taken subjects related to electronics & communication engineering and has been proficient in teaching subjects like Pulse & Digital Circuits, Microprocessors & Micro Controllers, VLSI Design, Electronic Devices & circuits, Electronic Circuit Analysis and many more.

She has published several papers in various journals which are indexed in Scopus and Various conferences.

She is a member of various professional groups like CSI, IAENG

Research Interests:

OPTIMIZED LOW POWER A/D CONVERTER FOR SIGNAL SPECIFIC APPLICATIONS, A LOW VOLTAGE SIGNAL SPECIFIC ADC, DESIGN OF LOW VOLTAGE LOW POWER ADC FOR WEARABLE AND BIOMEDICAL APPLICATION

Achievements:

Published a paper in **JOURNAL OF CRITICAL REVIEWS** titled “**OPTIMIZED LOW POWER A/D**

CONVERTER FOR SIGNAL SPECIFIC APPLICATIONS” published in SPRINGER Journal.

Published a paper in **JOURNAL OF CRITICAL REVIEWS** titled “**A LOW VOLTAGE SIGNAL SPECIFIC ADC” published in SPRINGER Journal.**

Published a paper in 2nd international Conference on Micro-Electronics, Electromagnetics and Telecommunications titled “**Designing Low Power Generator circuit with Switching Theory for Power Reduction” published in SPRINGER Journal.**

Published a paper in IJERT International journal titled “**Particle Swarm Optimization Approach for MST Problem in VLSI Routing”**

Ratified by JNTU Kakinada in December 2014

Guided an innovative project titled “**MULTI-MODE ELECTRONIC VOTING MACHINE” published in all newspapers and news channels.**