

**Mr.P. Sri Venkatesh**

Assistant Professor

Department

Electrical and Electronics Engineering

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Paper Published on " Title: **Simulation of High Step Up Interleaved Dc-Dc Converter For Micro Grid Application.**" presented in international journal for innovative engineering and management research (IJIMER) Volume 07, Issue 09, Page No: 101 – 112.

Paper Published on " Title: **A Model of PV Generation Suitable for Stability Analysis.**" presented in National power systems conference 2012.

Academic Background

M.E (Power System & Automation) – Andhra University

B.Tech (EEE) – Vitam college of engineering

Mr. P.Sri Venkatesh has rich experience in the field of power system engineering. He has taken subjects related to power system engineering and has been proficient in teaching subjects like power systems, stability & control, control systems & Electromagnetic field theory.

He has published 02 papers in various journals which are indexed in some reputed journals IJIMER.

He is a member of professional group of MISTE

Research Interests:

Smart grid, Power system optimization techniques, Power system stability & control and Multi level inverters.

Achievements: