

Academic Background

M.Tech (VLSI) – Sankethika Vidhya Parishad Engineering College.

B.Tech (Electronics & Communication Engineering) – Pydah College of Engineering & Technology.

Mr. H K Raghu Vamsi Kudulla has rich experience in the field of thermal engineering. He has taken subjects related to Electronics Engineering and has been proficient in teaching subjects like Digital Electronics, Analog Communications, Digital IC Applications, Linear IC Applications and Pulse and Digital Circuits.

He has published 8 papers in various journals which are indexed in Scopus and Science Citation Indexing and UGC.

Research Interests:

Low Power VLSI Design , Approximate Computing Techniques.

Achievements:

Qualified as Assistant Professor in UGCNET Exam Conducted in June, 2019.

Secured Silver in the NPTEL Course of Switching theory and Logic Design.