

Mr. P.Sekhar Babu

Assistant Professor

Electronics and Communication
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Academic Background

M.Tech (VLSI System Design) – MVGR college of Engineering

B.Tech (Electronics and Communication Engineering) – Pydha Institute of Engineering & Technology

Mr. P.Sekhar Babu has rich experience in the field of VLSI. He has taken subjects related to VLSI and Signal Processing and has been proficient in teaching subjects like VLSI, EDC and Digital image processing.

He has published 5 papers in various international journals.

Research Interests:

Artix-7 FPGA implementation and Image processing.

Publications:

- Published paper on “Algorithm to Image Identification and Artix-7 FPGA Implementation for Adequate Edge Detection” IJRTE Volume-8 Issue-5, January 2020.
- Published paper on “Typical Implementation of VITERBI Decoder for efficient error detection and correction” IOSR, Volume 8, Issue 5, May 2018.
- Published paper on “Fast Fourier Transform Pipeline Architecture for OFDM”, IJAREEIE, Volume 3, Issue 11, November 2014.
- Published paper on “Design of Hybrid Logic 4-Bit Comparator With Efficient VLSI Design Constraints” JSJ.U Volume-16 Issue-10, October 2020
- Published paper on “CPW –FED TEXTILE Antenna for Wearable Wireless Communication Applications” IJAASR Volume-5 Issue-2 , 2020