**Mr. Ch. Naveen Kumar**

Assistant Professor

**Department**

Electronics and Communication Engineering

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**Academic Background**

Pursuing Ph.D. in GITAM

M.Tech (VLSI System design) – Raghu engineering college

B.Tech (Electronics and Communication Engineering)

Mr.Ch.Naveen Kumar has rich experience in the field of VLSI system design. He has taken subjects related to Wireless Communications and VLSI system design and has been proficient in teaching subjects like Digital electronics, Wireless communications, sensors and networks, and VLSI design.

He has published 4 papers in various international journals.

**Research Interests:**

wireless communications and networks and Internet of Things.

**Paper publications:**

➢ Publish a paper in Scopus Journal on Algorithm to Image Identification and Artix-7 FPGA Implementation for Adequate Edge Detection(IIJRTE-February-2020)

➢ Publish a paper in International Journal on Design and Implementation of 4-bit Comparator by Using Wincell Partition with Efficient Constraints. (IJAEMA-May-2020)

➢ Presented a paper in International Conference on Advanced in computer science and software Engineering (ICACSSE 2014).

➢ Publish a paper in International Journal on Advanced in computer science and software engineering(IJACSSE 2014).

**Patent:**

Published patent on Software Defined Networking System for the Automation of Network Security.