

Ms. K. Geethanjali
Assistant Professor
Department
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Academic Background

M.Tech (VLSI) – MVGR College of Engineering &Technology

B.Tech (Electronics and Communication Engineering) – St. Theressa Institute of Engineering & Techology

Ms. K. Geethanjali have experience in the field of VLSI. He has taken subjects related to VLSI and has been proficient in teaching subjects like Pulse And Digital Circuits (PDC), Digital IC & Applications (DICA), Computer Networks (CN) and Electronic Measurement & Instrumentation (EMI)

She has published 5 papers in various journals which are indexed in Scopus and UGC Indexing.

Research Interests:

VLSI, Communications, Wireless Sensor Networks and Digital & Image Processing.

Achievements:

Qualified for Ph.D through *AURCET-2017* entrance exam.

Qualified for Ph.D through *APRCET-2020* entrance exam.