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## **Academic Background:**

Dr. A. Vamsidhar received his B.Tech in Electronics and Communication Engineering from JNTU, Kakinada in 2000 and M.Tech in Digital Systems and Communications from National Institute of Technology, Calicut, Kerala in 2003. He received his Ph.D in Electronics and Communication Engineering from Andhra University, Visakhapatnam, India in 2018. He is presently working as Associate Professor in the Department of Electronics and Communication Engineering, Raghu Engineering College (A), Visakhapatnam, India. He has more than 18 years of teaching experience in various academic institutions. He published more than 25 papers in various international and National journals as well as National & International Conferences. He also published an Indian Patent on "Peak to Average Power Ratio Reduction Method for MIMO-OFDM System". His areas of research include Signal Processing, Image & Video Processing, and Wireless Communications. He is a member of various professional bodies like IEEE, ISTE, IETE, Indian Science Congress Association (ISCA), Institution of Engineers (IE) etc.